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#### **Inventor Name Search Result**

Your Search was:

Last Name = BRUNSWICK

First Name = BRIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 8
60294446	Not Issued	159	05/30/2001	ATTACHMENT MECHANISM	BRUNSWICK, BRIAN A
<u>60208178</u>	Not Issued	159	05/31/2000	ATTACHMENT MECHANISM	BRUNSWICK, BRIAN A
<u>10451982</u>	Not Issued	030	09/24/2003	MIXER WITH PIVOTABLE BOWL	BRUNSWICK, BRIAN A
10329257	Not Issued	041	12/23/2002	POWER BOWL LIFT, MOTOR MOUNT AND TENSIONER FOR MIXING MACHINE	BRUNSWICK, BRIAN A.
10311938	Not Issued	030	03/05/2003	METHOD FOR REDUCING CODE ARTIFACTS IN BLOCK CODED VIDEO SIGNALS	BRUNSWICK, BRIAN DAVID
10155447	Not Issued	071	05/24/2002	ATTACHMENT MECHANISM	BRUNSWICK, BRIAN A.
10039930	Not Issued	071	10/27/2001	MIXER WITH PIVOTABLE BOWL	BRUNSWICK, BRIAN A.
09561828	<u>6494610</u>	150	04/28/2000	PIVOTABLE BOWL MOUNTING MECHANISM	BRUNSWICK, BRIAN A.

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another:	brunswick	brian	
Inventor		Search	Ушинионновичения.

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#### **Inventor Name Search Result**

Your Search was:

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60568908	Not Issued	020	05/07/2004	MIXER WITH PIVOTABLE BOWL	HUANG, JOSEPH C.
60558913	Not Issued	020	04/01/2004	90NM FPGA I/O BUFFER DESIGN WITH 1.6GBPS DATA RATE FOR SOURCE-SYNCHRONOUS SYSTEM AND 300MHZ CLOCK RATE FOR EXTERNAL MEMORY INTERFACE	HUANG, JOSEPH
60518564	Not Issued	020	11/07/2003	QUICK OIL CHANGE APPARATUS AND PROCESS	HUANG, JOSEPH
60315985	Not Issued	159	08/29/2001	ENHANCED DQS CLOCK ARCHITECTURE WITH PRECISE PHASE SHIFT CONTROL	HUANG, JOSEPH
60315904	Not Issued	159	08/29/2001	PROGRAMMABLE HIGH SPEED I/O INTERFACE	HUANG, JOSEPH
60315879	Not Issued	159	08/29/2001	DDR CLOCK SCHEME FOR PIN MIGRATION IN PROGRAMMABLE LOGIC DEVICE	HUANG, JOSEPH
60315876	Not Issued	159	08/29/2001	SELF-COMPENSATING DELAY CHAIN FOR DOUBLE DATE RATE (DDR) SDRAM APPLICATIONS IN PROGRAMMABLE LOGIC	HUANG, JOSEPH
60211936	Not Issued	159	06/15/2000	INNOVATIVE USE OF DUAL-PORT SRAM IN A PROGRAMMABLE LOGIC DEVICE	HUANG, JOSEPH
60209008	Not Issued	159	06/01/2000		HUANG, JOSEPH
<u>10857729</u>	Not Issued	020	06/01/2004	ULTRASONIC PIGMENT APPLICATION DEVICE	HUANG, JOSEPH

1084236	9 Not Issued	1 030	05/10/2004	MIXING DEVICE WITH VARIABLE SPEED DRIVE AND RELATED CONTROL FEATURES	HUANG, JOSEPH C
1080719	8 Not Issued	092	03/24/2004	KEY COLLECTING DEVICE CONTROLLED BY PULLING TRANSVERSELY	HUANG, JOSEPH
1080663	8 Not Issued	030	03/22/2004	ADDRESS CONTROL FOR EFFICIENT MEMORY PARTITION	HUANG, JOSEPH
10799408	Not Issued	030	03/12/2004	CONTROL CIRCUIT FOR SELF-COMPENSATING DELAY CHAIN FOR MULTIPLE-DATE-RATE INTERFACES	HUANG, JOSEPH
1078822	Not Issued	020	02/25/2004	DLL WITH ADJUSTABLE PHASE SHIFT USING PROCESSED CONTROL SIGNAL	HUANG, JOSEPH
10769733	Not Issued	030	01/29/2004	DATA REALIGNMENT TECHNIQUES FOR SERIAL-TO-PARALLEL CONVERSION	HUANG, JOSEPH
<u>10759915</u>	_ Not Issued	030	01/16/2004	LOOP CIRCUITRY WITH LOW-PASS NOISE FILTER	HUANG, JOSEPH
10757928	Not Issued	030	01/15/2004	APPARATUS AND METHODS FOR PROVIDING REDUNDANCY IN INTEGRATED CIRCUITS	HUANG, JOSEPH
10702300	Not Issued	093	11/05/2003	PROGRAMMABLE PHASE SHIFT CIRCUITRY	HUANG, JOSEPH
10696209	_ Not Issued	030	10/28/2003	DUAL-PORT SRAM IN A PROGRAMMABLE LOGIC DEVICE	HUANG, JOSEPH
<u>10685355</u>	Not Issued	092	10/09/2003	PROGRAMMABLE I/O ELEMENT CIRCUIT FOR HIGH SPEED LOGIC DEVICES	HUANG, JOSEPH
<u>10684341</u>	Not Issued	020	10/10/2003	PCI-COMPATIBLE PROGRAMMABLE LOGIC DEVICES	HUANG, JOSEPH
10678498	Not Issued	092	10/03/2003		HUANG, JOSEPH
10646538	Not Issued	030	08/22/2003	TECHNIQUES FOR PRELOADING DATA INTO MEMORY ON PROGRAMMABLE CIRCUITS	***************************************
10451982	Not Issued	030	09/24/2003		HUANG, JOSEPH C
10056787	6667641	150	01/23/2002	PROGRAMMABLE PHASE SHIFT	HUANG,

				CIRCUITRY	JOSEPH
10056367	Not Issued	094	01/25/2002	INI OI DOII DICI OICI O	HUANG, JOSEPH
1004693 <u>9</u>	<u>6747903</u>	150	01/14/2002	CONFIGURABLE DECODER FOR ADDRESSING A MEMORY	HUANG, JOSEPH
10043620	6549045	150	01/11/2002	CIRCUIT FOR PROVIDING CLOCK SIGNALS WITH LOW SKEW	HUANG, JOSEPH
10039930	Not Issued	071	10/27/2001	MIXER WITH PIVOTABLE BOWL	HUANG, JOSEPH C.
<u>10038737</u>	6806733	150	01/02/2002	MULTIPLE DATA RATE INTERFACE ARCHITECTURE	HUANG, JOSEPH
10037716	Not Issued	071	11/09/2001	ON/OFF REFERENCE VOLTAGE SWITCH FOR MULTIPLE I/O STANDARDS	HUANG, JOSEPH
10017933	Not Issued	061	12/14/2001	SCHMITT TRIGGER CIRCUIT WITH ADJUSTABLE TRIP POINT VOLTAGES	HUANG, JOSEPH
<u>10017666</u>	6686769	150	12/14/2001	PROGRAMMABLE I/O ELEMENT CIRCUIT FOR HIGH SPEED LOGIC DEVICES	HUANG, JOSEPH
09935792	Not Issued	041	08/22/2001	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
09898552	Not Issued	162	07/03/2001	PCI-COMPATIBLE PROGRAMMABLE LOGIC DEVICES	HUANG, JOSEPH
09883087	6661733	150	06/15/2001	DUAL-PORT SRAM IN A PROGRAMMABLE LOGIC DEVICE	HUANG, JOSEPH
<u>09882819</u>	Not Issued	1 160	06/15/2001	DUAL-PORT SRAM IN A PROGRAMMABLE LOGIC DEVICE	HUANG, JOSEPH
09865227	6366121	150	05/25/2001	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT ARCHITECTURES	HUANG, JOSEPH
09863143	6433579	150	05/22/2001		HUANG, S JOSEPH
09855865	6437650	150	05/15/2001	PHASE-LOCKED LOOP OR DELAY-LOCKED LOOP CIRCUITRY FOR PROGRAMMABLE LOGIC DEVICES	HUANG, JOSEPH

09846409	6335636	150	05/01/2001	PROGRAMMABLE LOGIC DEVICE INPUT/OUTPUT CIRCUIT CONFIGURABLE AS REFERENCE VOLTAGE INPUT CIRCUIT	HUANG, JOSEPH
09843299	6400598	150	04/25/2001	(4) (4) (5) (5) (6) (6) (6) (7) (8) (8) (8) (8) (8) (9) (9) (9) (9) (9) (9) (9) (9) (9) (9	HUANG, JOSEPH
09738403	6384629	150	12/15/2000	HIGH-SPEED PROGRAMMABLE INTERCONNECT	HUANG, JOSEPH
09736065	6271729	150	12/13/2000	PHASE-LOCKED LOOP OR DELAY-LOCKED LOOP CIRCUITRY FOR PROGRAMMABLE LOGIC DEVICES	HUANG, JOSEPH
09731473	6642758	150	12/05/2000	VOLTAGE, TEMPERATURE, AND PROCESS INDEPENDENT PROGRAMMABLE PHASE SHIFT FOR PLL	HUANG, JOSEPH
09684148	6392438	150	10/06/2000	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
<u>09677451</u>	Not Issued	161	10/02/2000	LIGHT SUPPORT STRUCTURE FOR CEILING FAN	HUANG, JOSEPH
09630071	6538469	150	07/31/2000	TECHNIQUE TO TEST AN INTEGRATED CIRCUIT USING FEWER PINS	HUANG, JOSEPH
09571766	6292116	150	05/16/2000	TECHNIQUES AND CIRCUITRY FOR ACCURATELY SAMPLING HIGH FREQUENCY DATA SIGNALS INPUT TO AN INTEGRATED CIRCUIT	HUANG, JOSEPH
09496945	Not Issued	161	02/03/2000	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH

	Last Name	First Name
Search Another:	huang	joseph
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#### **Inventor Name Search Result**

Your Search was:

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60049275	Not Issued	159	06/10/1997	METHOD OF IMPLEMENTING PLD PROGRAMMING MODE SELECTION WITH JTAG CIRCUITS	HUANG , JOSEPH
60049247	Not Issued	159	06/10/1997	PROGRAMMABLE JTAG FOR PLD WITHOUT DEDICATED JTAG PINS	HUANG , JOSEPH
60049246	Not Issued	159	06/10/1997	METHOD OF PROGRAMMING CASCADED SRAM BASED PLD WITHOUT DEDICATED PINS FOR HANDSHAKING	HUANG , JOSEPH
60049245	Not Issued	159	06/10/1997	CIRCUIT IMPLEMENTATION FOR FAST LINE TO REDUCE CLOCK SKEW AND GENERATE ASYNCHRONOUS CLOCK FROM LE	HUANG , JOSEPH
60049243	Not Issued	159	06/10/1997	CIRCUIT IMPLEMENTATION FOR HIGH SPEED PROGRAMMABLE INTERCONNECT AND UNIFIED PROGRAMMING INTERFACE	HUANG , JOSEPH
60038289	Not Issued	159	02/20/1997	PCI COMPATIBLE PLD	HUANG , JOSEPH
60038288	Not Issued	159	02/20/1997	LOGIC ARRAY BLOCK RESOURCES ON PLD	HUANG , JOSEPH
60038287	Not Issued	159	02/20/1997	METHODS AND APPARATUS TO PROVIDE SIGNALS TO AND FROM LABS ON PLD	HUANG , JOSEPH
<u>60015267</u>	Not Issued	159	04/11/1996	PROGRAMMABLE LOGIC DEVICE	HUANG , JOSEPH
<u>29178992</u>	Not Issued	041	04/02/2003	PROFILE OF AN INTERFACE OPENING OF A MIXING ELEMENT FOR A MIXER	HUANG, JOSEPH C.

<u>29173285</u>	D490271	150	12/23/2002		HUANG, JOSEPH C.
10886015	Not Issued	019	07/06/2004	2:	HUANG, JOSEPH
10731279	Not Issued	020	12/08/2003		HUANG, JOSEPH
10623394	Not Issued	030	07/18/2003	MULTIPLE DATA RATE INTERFACE ARCHITECTURE	HUANG, JOSEPH
10412705	6731142	150	04/10/2003		HUANG, JOSEPH
10405900	Not Issued	020	04/02/2003	MIXING ELEMENT WITH KEYWAY ATTACHMENT AND MIXER INCLUDING MIXING ELEMENT	HUANG, JOSEPH C.
10400191	Not Issued	061	03/25/2003	VOLTAGE, TEMPERATURE, AND PROCESS INDEPENDENT PROGRAMMABLE PHASE SHIFT FOR PLL	HUANG, JOSEPH
10361477	Not Issued	095	02/06/2003	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
10356691	Not Issued	041	01/31/2003	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
10350551	Not Issued	041	01/22/2003	PROGRAMMABLE LOGIC INTERGRATED CIRCUIT DEVICES WITH LOW VOLTAGE DIFFERENTIAL SIGNALING CAPABILITIES	HUANG, JOSEPH
10329040	Not Issued	041	12/23/2002	MIXING DEVICE WITH VARIABLE SPEED DRIVE AND RELATED CONTROL FEATURES	HUANG, JOSEPH C.
<u>10328536</u>	Not Issued	041	12/23/2002	MIXING MACHINE WITH ACCESSIBLE INTERNAL CONTROL UNIT	HUANG, JOSEPH C.
10269370	6707399	150	10/10/2002	DATA REALIGNMENT TECHNIQUES FOR SERIAL-TO-PARALLEL CONVERSION	HUANG, JOSEPH
10266092	Not Issued	071	10/04/2002	PHASE-LOCKED LOOP CIRCUITRY FOR PROGRAMMABLE LOGIC DEVICES	HUANG, JOSEPH

10229342	Not Issued	094	08/26/2002		HUANG, JOSEPH
10175980	6681378	150	06/19/2002	PROGRAMMING MODE SELECTION WITH JTAG CIRCUITS	HUANG, JOSEPH
10160355	<u>6617884</u>	150	05/28/2002	FAST LOCKING PHASE FREQUENCY DETECTOR	HUANG, JOSEPH
10147200	<u>6646467</u>	150	05/17/2002	PCI-COMPATIBLE PROGRAMMABLE LOGIC DEVICES	HUANG, JOSEPH
10146438	6535031	150	05/13/2002	PROGRAMMABLE LOGIC INTEGRATED CIRCUIT DEVICES WITH LOW VOLTAGE DIFFERENTIAL SIGNALING CAPABILITIES	HUANG, JOSEPH
10131976	<u>6731137</u>	150	04/24/2002	PROGRAMMABLE, STAGED, BUS HOLD AND WEAK PULL-UP FOR BI-DIRECTIONAL I/O	HUANG, JOSEPH
10118599	Not Issued	161	04/08/2002	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
10106675	6766505	150	03/25/2002	PARALLEL PROGRAMING OF PROGRAMMABLE LOGIC USING REGISTER CHAINS	HUANG, JOSEPH
10106673	6714044	150	03/25/2002	HI-SPEED PARALLEL CONFIGURATION OF PROGRAMMABLE LOGIC	HUANG, JOSEPH
10038737	<u>6806733</u>	150	01/02/2002	MULTIPLE DATA RATE INTERFACE ARCHITECTURE	HUANG, JOSEPH
10037861	Not Issued	030	01/02/2002	SELF-COMPENSATING DELAY CHAIN FOR MULTIPLE-DATE-RATE INTERFACES	HUANG, JOSEPH
09887686	6630844	150	08/22/2001	SUPPLY VOLTAGE DETECTION CIRCUIT	HUANG, JOSEPH
09285180	6114915	150	03/23/1999	PROGRAMMABLE WIDE-RANGE FREQUENCY SYNTHESIZER	HUANG, JOSEPH
09208124	6204688	150	12/09/1998	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES WITH INTERLEAVED LOGIC ARRAY BLOCKS	HUANG , JOSEPH
09184383	6134173	150	11/02/1998	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
09169332	6018490	150	10/09/1998	PROGRAMMABLE LOGIC	HUANG,

				ARRAY INTEGRATED CIRCUITS	JOSEPH
09023369	6049225	150	02/13/1998	INPUT/OUTPUT INTERFACE CIRCUITRY FOR PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08919988	6127844_	150	08/28/1997	PCI-COMPATIBLE PROGRAMMABLE LOGIC DEVICES	HUANG , JOSEPH
08902416	<u>5999015</u>	150	07/29/1997	LOGIC REGION RESOURCES FOR PROGRAMMABLE LOGIC DEVICES	HUANG, JOSEPH
08672676	5909126	150	06/28/1996	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES WITH INTERLEAVED LOGIC ARRAY BLOCKS	HUANG , JOSEPH
08664919	5883850	150	06/18/1996	PROGAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
08655870	<u>5668771</u>	150	05/24/1996	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
08442795	5689195	150	05/17/1995	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
08349890	5544415	150	12/06/1994	WATER-PROOF AND WASHABLE ELECTRIC RAZOR	HUANG , JOSEPH
08035406	Not Issued	164	03/23/1993	MULTI-PURPOSE SATCHEL	HUANG, JOSEPH
07831378	Not Issued	164	02/03/1992	PORTABLE DRAFTING TABLE	HUANG , JOSEPH
07218845	4893506	150	07/14/1988	METHOD FOR ESTIMATING RAINFALL AND A SENSING DEVICE THEREFOR	HUANG, JOSEPH

	Last Name	First Name
Search Another:	huang	joseph
Inventor		Search

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#### **Inventor Name Search Result**

Your Search was:

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60134437	Not Issued	159	05/17/1999	METHOD FOR USING OVER SAMPLING WITH MULTIPLE CLOCK STROBES TO ACHIEVE ACCURATE DATA SAMPLING STROBE IN A PLD	HUANG, JOSEPH
60124157	Not Issued	159	03/10/1999	METHOD AND APPARATUS FOR SIMPLIFIED CREATION AND EDITING OF COMPUTER GRAPHICS	HUANG , JOSEPH
60118203	Not Issued	159	02/01/1999	METHOD FOR USING I/O PINS AS REFERENCE VOLTAGE PINS IN A NOISY ENVIRONMENT IN A PROGRAMMABLE LOGIC DEVICE	HUANG , JOSEPH
60115263	Not Issued	159	01/08/1999	INNOVATED PLL NETWORK COMPENSATION TO ACHIEVE ZERO DELAY BUFFER	HUANG , JOSEPH
60115238	Not Issued	159	01/08/1999	INNOVATED PLL WITH FREQUENCY SYNTHESIS, PROGRAMMABLE PHASE ADJUSTMENT AND MULTIPLE OUTPUTS	HUANG , JOSEPH
60115214	Not Issued	159	01/08/1999	PROCESS INDEPENDENT LVDS OUTPUT DRIVER IN A PROGRAMMABLE LOGIC DEVICE	HUANG , JOSEPH
60115213	Not Issued	159	01/08/1999	INNOVATED INPUT COMMON MODE VOLTAGE COMPENSATED BIAS CIRCUIT	HUANG, JOSEPH
60107166	Not Issued	159	11/05/1998	WIDE RANGE FREQUENCY SYNTHESIZER	HUANG , JOSEPH

				PHASE AND FREQUENCY DETECTOR	JOSEPH
60106876	Not Issued	159	11/03/1998	PROGRAMMABLE PHASE SHIFT CIRCUIT	HUANG , JOSEPH
<u>60091617</u>	Not Issued	159	07/02/1998	INNOVATED AC KICKER FOR HIGH SPEED LOW VOLTAGE SWING DIFFERENTIAL OUTPUT BUFFER WITH TEMPERATURE COMPENSATION	HUANG , JOSEPH
60091524	Not Issued	159	07/02/1998	NEW CIRCUIT FOR HIGH SPEED LOW VOLTAGE DIFFERENTIAL INPUTS WITH HYSTERESIS CONTROL AND OPEN-SHORT PROTECTION	HUANG , JOSEPH
60052990	Not Issued	159	06/10/1997	METHOD TO REDUCE NUMBER OF TEST PINS FOR LOW PIN COUNT PACKAGES IN PLD	HUANG , JOSEPH
60050953	Not Issued	159	06/13/1997	CIRCUIT IMPLEMENTATION FOR HIGH SPEED PROGRAMMABLE INTERCONNECT AND UNIFIED PROGRAMMING INTERFACE	HUANG, JOSEPH
60049478	Not Issued	159	06/13/1997	A METHOD OF IMPLEMENTING PLD PROGRAMMING MODE SELECTION WITH JTAG CIRCUITS	HUANG , JOSEPH
60049275	Not Issued	159	06/10/1997	METHOD OF IMPLEMENTING PLD PROGRAMMING MODE SELECTION WITH JTAG CIRCUITS	HUANG, JOSEPH
09432442	6448820	150	11/02/1999	FAST LOCKING PHASE FREQUENCY DETECTOR	HUANG , JOSEPH
09432142	6369624	150	11/02/1999	PROGRAMMABLE PHASE SHIFT CIRCUITRY	HUANG , JOSEPH
<u>09405556</u>	6154055	150	09/24/1999	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
<u>09395886</u>	6271681	150	09/14/1999	֥ :	HUANG , JOSEPH
09393036	6177844	150	09/09/1999	PHASE-LOCKED LOOP OR DELAY-LOCKED LOOP CIRCUITRY FOR PROGRAMMABLE LOGIC	HUANG; JOSEPH HUANG; JOSEPH HUANG; JOSEPH HUANG; JOSEPH HUANG; JOSEPH

				DEVICES	
09366940	6483886	150	08/04/1999	PHASE-LOCKED LOOP CIRCUITRY FOR PROGRAMMABLE LOGIC DEVICES	HUANG , JOSEPH
<u>09366937</u>	6346827	150	08/04/1999	PROGRAMMABLE LOGIC DEVICE INPUT/OUTPUT CIRCUIT CONFIGURABLE AS REFERENCE VOLTAGE INPUT CIRCUIT	HUANG, JOSEPH
09345434	6246270	150	07/01/1999	INNOVATED AC KICKER FOR HIGH SPEED LOW VOLTAGE SWING DIFFERENTIAL OUTPUT BUFFER WITH TEMPERATURE COMPENSATION	HUANG , JOSEPH
09340222	6236231	150	06/25/1999	PROGRAMMABLE LOGIC INTEGRATED CIRCUIT DEVICES WITH LOW VOLTAGE DIFFERENTIAL SIGNALING CAPABILITIES	HUANG , JOSEPH
<u>09179254</u>	6064599	150	10/26/1998	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
09156036	6023439	150	09/17/1998	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
09096917	Not Issued	095	06/12/1998	VARIABLE DEPTH AND WIDTH MEMORY DEVICE	HUANG , JOSEPH
09096251	6128692	150	06/11/1998	PROGRAMMING AND VERIFICATION ADDRESS GENERATION FOR RANDOM ACCESS MEMORY BLOCKS IN PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH HUANG, JOSEPH HUANG, JOSEPH HUANG, JOSEPH HUANG, JOSEPH
09094356	<u>6262595</u>	150	06/09/1998	HIGH-SPEED PROGRAMMABLE INTERCONNECT	HUANG , JOSEPH
09094226	6314550	150	06/09/1998	CASCADED PROGRAMMING WITH MULTIPLE-PURPOSE PINS	HUANG , JOSEPH
09094221	6691267	150	06/09/1998	TECHNIQUE TO TEST AN INTEGRATED CIRCUIT USING FEWER PINS	HUANG , JOSEPH
09094186	6421812	150	06/09/1998	PROGRAMMING MODE SELECTION WITH JTAG	HUANG , JOSEPH

09010384	RE37060	150	01/21/1998	CIRCUITS APPARATUS FOR SERIAL	HUANG,
<u>09010364</u>	<u>KE37000</u>	130	01/21/1990	READING AND WRITING OF RANDOM ACCESS MEMORY ARRAYS	JOSEPH
<u>08970830</u>	<u>5986470</u>	150	11/14/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08873169	<u>5982195</u>	150	06/11/1997	PROGRAMMABLE LOGIC DEVICE ARCHITECTURES	HUANG , JOSEPH
08852015	5764583	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
<u>08851862</u>	6028808	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
<u>08851858</u>	5848005	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTERGRATED CIRCUITS	HUANG, JOSEPH
08851761	5812479	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
08847004	5828229	150	05/01/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
08840534	5838628	150	04/22/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
08834998	5850152	150	04/07/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
08834996	5850151	150	04/07/1997	PROGRAMMABLE LOGIC ARRAY INTERGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08807561	5963049	150	02/28/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT ARCHITECTURES	HUANG, JOSEPH
08555283	<u>5802540</u>	150	11/08/1995	PROGRAMMING AND VERIFICATION ADDRESS GENERATION FOR RANDOM ACCESS MEMORY BLOCKS IN PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08555110	5555214	150	11/08/1995	APPARATUS FOR SERIAL READING AND WRITING OF RANDOM ACCESS MEMORY	HUANG , JOSEPH

				ARRAYS	
08555109	<u>5717901</u>	150	11/08/1995	VARIABLE DEPTH AND WIDTH MEMORY DEVICE	HUANG , JOSEPH
<u>08555106</u>	5633830	150	11/08/1995	RANDOM ACCESS MEMORY BLOCK CIRCUITRY FOR PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
08519045	5764080	150	08/24/1995	INPUT/OUTPUT INTERFACE CIRCUITRY FOR PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08245509	5550782	150	05/18/1994	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH

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Your Search was:

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60134437	Not Issued	159	05/17/1999	METHOD FOR USING OVER SAMPLING WITH MULTIPLE CLOCK STROBES TO ACHIEVE ACCURATE DATA SAMPLING STROBE IN A PLD	HUANG , JOSEPH
60124157	Not Issued	159	03/10/1999	METHOD AND APPARATUS FOR SIMPLIFIED CREATION AND EDITING OF COMPUTER GRAPHICS	HUANG , JOSEPH
60118203	Not Issued	159	02/01/1999	METHOD FOR USING I/O PINS AS REFERENCE VOLTAGE PINS IN A NOISY ENVIRONMENT IN A PROGRAMMABLE LOGIC DEVICE	HUANG , JOSEPH
60115263	Not Issued	159	01/08/1999	INNOVATED PLL NETWORK COMPENSATION TO ACHIEVE ZERO DELAY BUFFER	HUANG , JOSEPH
60115238	Not Issued	159	01/08/1999	INNOVATED PLL WITH FREQUENCY SYNTHESIS, PROGRAMMABLE PHASE ADJUSTMENT AND MULTIPLE OUTPUTS	HUANG , JOSEPH
60115214	Not Issued	159	01/08/1999	PROCESS INDEPENDENT LVDS OUTPUT DRIVER IN A PROGRAMMABLE LOGIC DEVICE	HUANG , JOSEPH
60115213	Not Issued	159	01/08/1999	INNOVATED INPUT COMMON MODE VOLTAGE COMPENSATED BIAS CIRCUIT	HUANG , JOSEPH
60107166	Not Issued	159	11/05/1998	WIDE RANGE FREQUENCY SYNTHESIZER	HUANG , JOSEPH
60107101	Not Issued	159	11/04/1998	PROGRAMMABLE IN-STATE	HUANG,

				PHASE AND FREQUENCY DETECTOR	JOSEPH
60106876	Not Issued	159	11/03/1998	PROGRAMMABLE PHASE SHIFT CIRCUIT	HUANG , JOSEPH
60091617	Not Issued	159	07/02/1998	INNOVATED AC KICKER FOR HIGH SPEED LOW VOLTAGE SWING DIFFERENTIAL OUTPUT BUFFER WITH TEMPERATURE COMPENSATION	HUANG , JOSEPH
60091524	Not Issued	159	07/02/1998	NEW CIRCUIT FOR HIGH SPEED LOW VOLTAGE DIFFERENTIAL INPUTS WITH HYSTERESIS CONTROL AND OPEN-SHORT PROTECTION	HUANG , JOSEPH
60052990	Not Issued	159	06/10/1997	METHOD TO REDUCE NUMBER OF TEST PINS FOR LOW PIN COUNT PACKAGES IN PLD	HUANG , JOSEPH
60050953	Not Issued	159	06/13/1997	CIRCUIT IMPLEMENTATION FOR HIGH SPEED PROGRAMMABLE INTERCONNECT AND UNIFIED PROGRAMMING INTERFACE	HUANG , JOSEPH
60049478	Not Issued	159	06/13/1997	A METHOD OF IMPLEMENTING PLD PROGRAMMING MODE SELECTION WITH JTAG CIRCUITS	HUANG , JOSEPH
60049275	Not Issued	159	06/10/1997	METHOD OF IMPLEMENTING PLD PROGRAMMING MODE SELECTION WITH JTAG CIRCUITS	HUANG , JOSEPH
09432442	6448820	150	11/02/1999	FAST LOCKING PHASE FREQUENCY DETECTOR	HUANG , JOSEPH
09432142	<u>6369624</u>	150	11/02/1999	PROGRAMMABLE PHASE SHIFT CIRCUITRY	HUANG , JOSEPH
09405556	6154055	150	09/24/1999	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
09395886	6271681	150	09/14/1999	PCI-COMPATIBLE PROGRAMMABLE LOGIC DEVICES	HUANG , JOSEPH
09393036	6177844	150	09/09/1999	PHASE-LOCKED LOOP OR DELAY-LOCKED LOOP CIRCUITRY FOR PROGRAMMABLE LOGIC	HUANG, JOSEPH

00266040	6102006	150	08/04/1999	PHASE-LOCKED LOOP	HUANG,
09366940	6483886	150	08/04/1999	5 ·	JOSEPH
<u>09366937</u>	6346827	150	08/04/1999	PROGRAMMABLE LOGIC DEVICE INPUT/OUTPUT CIRCUIT CONFIGURABLE AS REFERENCE VOLTAGE INPUT CIRCUIT	HUANG , JOSEPH
09345434	6246270	150	07/01/1999	INNOVATED AC KICKER FOR HIGH SPEED LOW VOLTAGE SWING DIFFERENTIAL OUTPUT BUFFER WITH TEMPERATURE COMPENSATION	HUANG , JOSEPH
09340222	6236231	150	06/25/1999	PROGRAMMABLE LOGIC INTEGRATED CIRCUIT DEVICES WITH LOW VOLTAGE DIFFERENTIAL SIGNALING CAPABILITIES	HUANG , JOSEPH
09179254	6064599	150	10/26/1998	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
09156036	6023439	150	09/17/1998	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
09096917	Not Issued	095	06/12/1998	VARIABLE DEPTH AND WIDTH MEMORY DEVICE	HUANG , JOSEPH
09096251	6128692	150	06/11/1998	PROGRAMMING AND VERIFICATION ADDRESS GENERATION FOR RANDOM ACCESS MEMORY BLOCKS IN PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
09094356	<u>6262595</u>	150	06/09/1998	HIGH-SPEED PROGRAMMABLE INTERCONNECT	HUANG , JOSEPH
09094226	<u>6314550</u>	150	06/09/1998	CASCADED PROGRAMMING WITH MULTIPLE-PURPOSE PINS	HUANG , JOSEPH
09094221	6691267	150	06/09/1998	TECHNIQUE TO TEST AN INTEGRATED CIRCUIT USING FEWER PINS	HUANG , JOSEPH
09094186	6421812	150	06/09/1998	PROGRAMMING MODE SELECTION WITH JTAG	HUANG, JOSEPH

				CIRCUITS	
09010384	RE37060	150	01/21/1998	APPARATUS FOR SERIAL READING AND WRITING OF RANDOM ACCESS MEMORY ARRAYS	HUANG , JOSEPH
08970830	<u>5986470</u>	150	11/14/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08873169	5982195	150	06/11/1997	PROGRAMMABLE LOGIC DEVICE ARCHITECTURES	HUANG , JOSEPH
08852015	5764583	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
08851862	6028808	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
08851858	5848005	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTERGRATED CIRCUITS	HUANG, JOSEPH
08851761	5812479	150	05/06/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
08847004	5828229	150	05/01/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG, JOSEPH
08840534	<u>5838628</u>	150	04/22/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH
08834998	<u>5850152</u>	150	04/07/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08834996	5850151	150	04/07/1997	PROGRAMMABLE LOGIC ARRAY INTERGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08807561	5963049	150	02/28/1997	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT ARCHITECTURES	HUANG , JOSEPH
08555283	<u>5802540</u>	150	11/08/1995	PROGRAMMING AND VERIFICATION ADDRESS GENERATION FOR RANDOM ACCESS MEMORY BLOCKS IN PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG , JOSEPH
08555110	5555214	150	11/08/1995	APPARATUS FOR SERIAL READING AND WRITING OF RANDOM ACCESS MEMORY	HUANG, JOSEPH

				ARRAYS	
08555109	<u>5717901</u>	150	11/08/1995	VARIABLE DEPTH AND WIDTH MEMORY DEVICE	HUANG , JOSEPH
<u>08555106</u>	5633830	150	11/08/1995	RANDOM ACCESS MEMORY BLOCK CIRCUITRY FOR PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
08519045	5764080	150	08/24/1995	INPUT/OUTPUT INTERFACE CIRCUITRY FOR PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUIT DEVICES	HUANG, JOSEPH
08245509	5550782	150	05/18/1994	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JOSEPH

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### **Inventor Name Search Result**

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 10
60169161	Not Issued	159	12/06/1999	METHOD FOR VOLTAGE, TEMPERATURE, AND PROCESS INDEPENDENT PROGRAMMABLE PHASE SHIFT IN PLL	HUANG, JOSEPH
09612050	Not Issued	161	07/07/2000	SYSTEM AND METHOD FOR TRADING GOODS	HUANG, JOSEPHINE
09235361	6092448	150	01/21/1999	SLICER WITH STAGED DYNAMIC BRAKING SYSTEM	HUANG , JOSEPH CHUNCHI
09156036	6023439	150	09/17/1998	PROGRAMMABLE LOGIC ARRAY INTEGRATED CIRCUITS	HUANG , JÖSEPH
08964984	Not Issued	161	11/05/1997	METHOD FOR MAKING CALIBRATION BUMP ON A SUBSTRATE UTILIZING A MODIFIED LIFT-OFF TECHNIQUE	HUANG , JOSEPH C
08784482	5862730	150	01/17/1997	SLICER WITH STAGED DYNAMIC BRAKING SYSTEM	HUANG , JOSEPH CHUNCHI
08736930	Not Issued	161	10/25/1996	SYSTEM AND METHOD FOR COURSE-INDEPENDENT WEB-BASED DELIVERY AND MANAGEMENT OF EDUCATIONAL MATERIAL	HUANG , JOSEPH C.
08425919	5666866	150	04/20/1995	FOOD PRODUCT SLICING MACHINE INCORPORATING A SCALE	HUANG , JOSEPH CHUN-CHI
08228561	5644239	150	04/15/1994	METHOD AND APPARATUS FOR SENSING THE CONDITION OF A FLUID	HUANG , JOSEPH Y.
06234837	4360624	150	02/13/1981	SMOKE AND FIRE RETARDANTS FOR	HUANG , JOSEPH M. G.

	`-	HALOGEN-CONTAINING PLASTIC COMPOSITIONS	
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#### **Inventor Name Search Result**

Your Search was:

Last Name = BADER First Name = BRIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 9
10842369	Not Issued	030	05/10/2004	MIXING DEVICE WITH VARIABLE SPEED DRIVE AND RELATED CONTROL FEATURES	BADER, BRIAN E
10451982	Not Issued	030	09/24/2003	MIXER WITH PIVOTABLE BOWL	BADER, BRIAN E
10329257	Not Issued	041	12/23/2002	POWER BOWL LIFT, MOTOR MOUNT AND TENSIONER FOR MIXING MACHINE	BADER, BRIAN E.
10329040	Not Issued	041	12/23/2002	MIXING DEVICE WITH VARIABLE SPEED DRIVE AND RELATED CONTROL FEATURES	BADER, BRIAN E.
10328536	Not Issued	041	12/23/2002	MIXING MACHINE WITH ACCESSIBLE INTERNAL CONTROL UNIT	BADER, BRIAN E.
10039930	Not Issued	071	10/27/2001	MIXER WITH PIVOTABLE BOWL	BADER, BRIAN E.
09235361	6092448	150	01/21/1999	SLICER WITH STAGED DYNAMIC BRAKING SYSTEM	BADER , BRIAN ERIC
08784482	<u>5862730</u>	150	01/17/1997	SLICER WITH STAGED DYNAMIC BRAKING SYSTEM	BADER , BRIAN ERIC
07131499	4813316	150	12/10/1987	CONTROL SYSTEM AND METHOD FOR A FOOD PRODUCT SLICER	BADER, BRIAN E.

Inventor Search Completed: No Records to Display.